

INTERNATIONAL SEARCH REPORT

International Application No
PCT/IB2004/051519

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F9/48 G06F9/30 G06F9/38

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, IBM-TDB, INSPEC, COMPENDEX

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No
X	HALSTEAD R H ET AL: "COMPUTING. MASA: A MULTITHREADED PROCESSOR ARCHITECTURE FOR PARALLEL SYMBOLIC-PROCEEDINGS OF THE ANNUAL INTERNATIONAL SYMPOSIUM ON COMPUTER ARCHITECTURE. HONOLULU, MAY 30 - JUNE 2, 1988, WASHINGTON, IEEE COMP. SOC. PRESS, US, vol. SYMP. 15, 30 May 1988 (1988-05-30), pages 443-451, XP000092207	1-3, 5-7
Y	page 445, right-hand column, line 22 - line 27 page 446, left-hand column, line 1 - line 13 page 446, left-hand column, line 33 - line 49 page 446, right-hand column, line 1 - line 2 ----- -/--	4,8-11

☒ Further documents are listed in the continuation of box C

☒ Patent family members are listed in annex

Special categories of cited documents

- "A" document defining the general state of the art which is not considered to be of particular relevance
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- "O" document referring to an oral disclosure, use, exhibition or other means
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"X" document of particular relevance, the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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Date of the actual completion of the international search

26 September 2005

Date of mailing of the international search report

04/10/2005

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C(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No
Y	MAHON M J ET AL: "HEWLETT-PACKARD PRECISION ARCHITECTURE: THE PROCESSOR" HEWLETT-PACKARD JOURNAL, HEWLETT-PACKARD CO. PALO ALTO, US, vol . 37, no. 8, 1 August 1986 (1986-08-01), pages 4-22, XP000211314 page 7, right-hand column, line 1 - page 9, left-hand column, line 14 -----	4, 8-11
A	US 6 233 599 B1 (NATION GEORGE WAYNE ET AL) 15 May 2001 (2001-05-15) column 3, line 1 - line 7; figures 3B, 3D column 7, line 11 - line 19 column 7, line 35 - line 55 column 8, line 40 - line 48 column 9, line 6 - last line column 16, line 42 - column 17, line 4 -----	1-3, 5-7
A	EP 0 696 772 A (ROCKWELL INTERNATIONAL CORPORATION; CONEXANT SYSTEMS, INC) 14 February 1996 (1996-02-14) column 5, line 12 - line 16 column 5, line 30 - line 46 column 7, line 12 - line 22 -----	1-3, 5-7
A	US 2002/095565 A1 (NEMIROVSKY MARIO D ET AL) 18 July 2002 (2002-07-18) paragraphs '0026 !, '0058 !, '0059 !; figures 1A, 2A -----	1-3, 5-7

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Information on patent family members

International Application No

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Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 6233599	B1	15-05-2001	NONE	
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			DE 69525294 T2	19-09-2002
			JP 8063361 A	08-03-1996
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